

12A Synchronous Class-H Boost Converter with Audio Detection

■ FEATURES

- \cdot Input voltage range V_{IN}: 2.7V to 12.2V
- Output voltage range Vout: 4.5V to 12.8V
- Programmable switch peak current limit: up to 12A
- · High Efficiency

 $\begin{array}{l} 92\% \; (V_{\text{IN}} = 7.4 \text{V}, \, V_{\text{OUT}} {=} 12.8 \text{V}, \, I_{\text{OUT}} {=} 3 \text{A}) \\ 92\% \; (V_{\text{IN}} = 3.6 \text{V}, \, V_{\text{OUT}} {=} 9 \text{V}, \, I_{\text{OUT}} {=} 1 \text{A}) \end{array}$

· Class-H boost converter with Audio detection

10 modes with power supply of 1 or 2 batteries in series, extremely extend the battery life when the output power is connected to an Audio Amp.

· 2 modulation mode available: PFM or PWM

mode at light load

- · 1.0µA current consumption during shutdown
- · Adjustable switching frequency: 200k to 1.4MHz
- · Programmable soft start
- · Output overvoltage protection (at 14V), cycle-by-

cycle overcurrent protection, thermal shutdown protection

• Pb-free Packages, DFN20L, 4.5mm*3.5mm

APPLICATIONS

· Wireless/ Speakers · Portable Speakers

DESCRIPTION

The HT71678 is a high-power density, fully integrated synchronous boost converter with a $16m\Omega$ power switch and a $23m\Omega$ rectifier switch to provide a high efficiency and small size solution in portable systems. The HT71678 has wide input voltage range from 2.7 V to 12.2 V to support applications with single cell and two cell Lithium batteries. The device has 12A switch current capability and can provide an output voltage up to 12.8V.

The HT71678 integrates audio detection which receives the audio signal to change the output voltage in accordance with audio amplitude, so that the system efficiency will be improved. There are 10 different modes that can be selected with different output voltages.

The HT71678 uses adaptive constant off-time peak current control topology to regulate the output voltage. In moderate to heavy load condition, it works in the PWM mode. In light load condition, the device has two operation modes selected by the MODE pin. One is PFM mode to improve the efficiency and another one is the forced PWM mode to avoid application problems caused by low switching frequency. The switching frequency in the PWM mode is adjustable ranging from 200kHz to 1.4MHz by an external resistor.

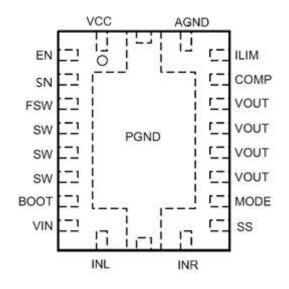
The HT71678 also implements a programmable soft-start function and an adjustable switching peak current limit function. In addition, the device provides 14V output overvoltage protection, cycle-by-cycle overcurrent protection, and thermal shutdown protection.

ORDERING INFORMATION

Part Number	Package Type	Marking	Operating Temperature Range	Shipping Package / MOQ
HT71678DNER	DFN20L	HT71678	-40°C∼85°C	Tape and Reel / 5000PCS



■ TERMINAL CONFIGURATION

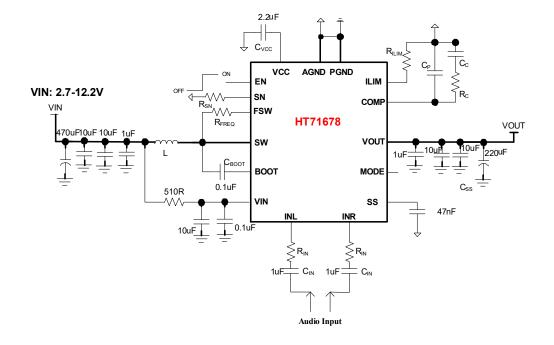


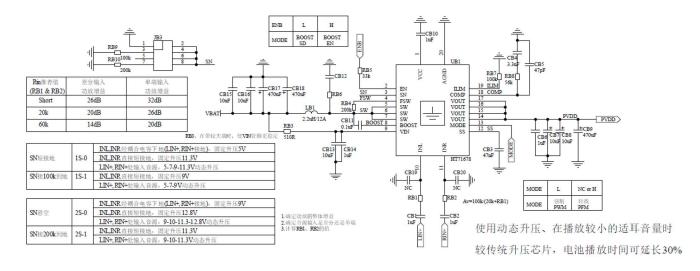
■ TERMINAL FUNCTION

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Terminal No.	NAME	I/O ^{*1}	Description	
1	VCC	0	Output of the internal regulator. A ceramic capacitor of 2.2uF is required between this pin and ground.	
2	EN	I	Enable logic input. Logic high level enables the device. Logic low level disables the device and turns it into shutdown mode.	
3	SN	0	Output voltage selection pin for boost converter.	
4	FSW	I	The switching frequency is programmed by a resister between this pin and the SW pin.	
5/6/7	SW	PWR	The switching node pin of the converter.	
8	BOOT	0	Power supply for high-side MOSTFET gate driver. A ceramic	
			capacitor of 0.1µF must be connected between this pin and the SW pin.	
9	VIN		IC power supply input.	
10	INL		Audio detection input pin of left channel	
11	INR		Audio detection input pin of right channel	
12	SS	0	Soft-start programming pin. An external capacitor connected to ground sets the ramp rate of the internal error amplifier's reference voltage during soft-start	
13	MODE	I	Operation mode selection pin for the device in light load condition. When this pin is connected to ground, the device works in PWM mode. When this pin is left floating, the device works in PFM mode.	
14/15/16/17	VOUT	PWR	Boost converter output.	
18	COMP	0	Output of the internal error amplifier, the loop compensation network should be connected between this pin and the AGND pin.	
19	ILIM	Ι	Adjustable switch peak current limit. An external resister should be connected between this pin and the AGND pin.	
20	AGND	-	Signal ground of the IC.	
0	PGND	PWR	Power ground of the IC.	



TYPICAL APPLICATION







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